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### Industrial ARM7 Processor

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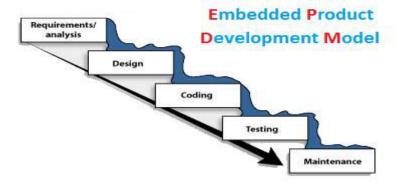


Embedded Systems with ARM7 Processor











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**Embedded Technosolutions** is a Professional Training Institute & a Company which Working for Indian MNCs & Medium/Small Scale Industries in Product R&D, Development, Manufacturing & Customization.

Our training sessions are purely practical, based on industrial standard.



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### Course Objectives

- 1. To provide professional and industrial standard training which will help the students to get job in the core industries
- 2. To learn practical orientation of actual industrial product design
- 3. To learn Embedded Systems which is the important part of Embedded Product Development
- 4. To learn Embedded C Programing which is the important part of Embedded Product Development
- 5. To learn Programming of Internal Peripherals in Embedded C
- 6. To learn ARM7 Architecture in detail (LPC2148)
- 7. To learn Hardware Requirements for LPC2148
- 8. To learn the designing of Real World Projects



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### **Course Syllabus**

#### **Chapter1**: Introduction to ARM Processor

- Introduction to embedded system and ARM Processor
- ARM processor family
- Application of ARM Processor
- Compiler
- Difference between RISC & CISC

#### **Chapter2**: LPC2148 Microcontroller Pin details, Memory

- LPC2148ARM7 microcontroller
- FeaturesofLPC2148
- BlockdiagramofLPC2148
- PindiagramofLPC2148
- Architectural overview
- On-chip flash program memory



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On-chip static RAM

#### **Chapter3**: Keil IDE

- Introduction to Keil IDE
- Creating project with Keil
- Debugging

#### **Chapter4**: Hardware Interface

- Minimum system requirements for LPC2148
- Hardware interfacing details

#### **Chapter5**: System Control

- PLL
- External Interrupt input



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#### **Chapter6**: Pin Connect block, GPIO

- Pin Connect Block
- General Purpose I/O:
- LED and switches interfacing
- Buzzer
- LCD
- DC Motor
- 4×4 Matrix Keypad

#### **Chapter7: UART**

• UART: Features, Serial Communication



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# Contact Us

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www.embeddedtechnosolutions.com

### **Our Other Ventures**

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